

**Notice of References Cited**

Application/Control No.

09/750,095

Applicant(s)/Patent Under

Reexamination

JOURDAN ET AL.

Examiner

Richard Ellis

Art Unit

2183

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**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,758,112	05-1998	Yeager et al.	712/217
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

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	N					
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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
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	V	Yeager, Kenneth C., The Mips R10000 superscalar microprocessor, IEEE Micro, April 1996, v. 16, i. 2, pp. 28-41.
	W	Moudgil, M. et al., Register renaming and dynamic speculation: an alternative approach, Proceedings of the 26th Annual International Symposium on Microarchitecture, 1-3 December 1993, pp. 202-213.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.